

Best Paper Candidates

ASP-DAC 2023

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ID	Paper Title	Authors
4B-1	Microarchitecture Power Modeling via Artificial Neural Network and Transfer Learning	Jianwang Zhai, Yici Cai, Bei Yu
1C-1	Safety-Driven Interactive Planning for Neural Network-based Lane Changing	Xiangguo Liu, Ruochen Jiao, Bowen Zheng, Dave Liang, Qi Zhu
4C-1	Mixed-Criticality with Integer Multiple WCETs and Dropping Relations: New Scheduling Challenges	Federico Reghenzani, William Fornaciari
2B-1	DECC: Differential ECC for Read Performance Optimization on High-Density NAND Flash Memory	Yunpeng Song, Yina Lv, Liang Shi
6B-1	Hardware-Software Codesign of DNN Accelerators using Approximate Posit Multipliers	Tom Glint, Kailash Prasad, Jinay Dagli, Krishil Gandhi, Aryan Gupta, Vrajesh Patel, Neel Shah, Joyce Mekié
1D-1	Fully Automated Machine Learning Model Development for Analog Placement Quality Prediction	Chen-Chia Chang, Jingyu Pan, Zhiyao Xie, Yaguang Li, Yishuang Lin, Jiang Hu, Yiran Chen
2C-2	Approximate Floating-Point FFT Design with Wide Precision-Range and High Energy Efficiency	Chenyi Wen, Ying Wu, Xunzhao Yin, Cheng Zhuo
3D-1	Rethink before Releasing Your Model: ML Model Extraction Attack in EDA	Chen-Chia Chang, Jingyu Pan, Zhiyao Xie, Jiang Hu, Yiran Chen
5D-3	Automatic Test Pattern Generation and Compaction for Deep Neural Networks	Dina A. Moussa, Michael Hefenbrock, Christopher Münch, Mehdi Tahoori
6C-1	Hardware Trojan Detection Using Shapley Ensemble Boosting	Zhixin Pan, Prabhat Mishra
3A-1	A SAT Encoding for Optimal Clifford Circuit Synthesis	Sarah Schneider, Lukas Burgholzer, Robert Wille